# **iDAQ-801**

# 4-ch, 24-bit, 256kS/s/ch, DSA iDAQ Module



## **Features**

- 4-ch simultaneous sampling up to 256kS/s
- 24-bit resolution
- Direct IEPE supply
- · Anti-aliasing filter equipped
- High gain up to ±187.5mV input range

Vertrieb durch

AMC - Analytik & Messtechnik GmbH Chemnitz

Tel.: +49/371/38388-0 Heinrich-Lorenz-Str. 55 09120 Chemnitz Fax: +49/371/38388-99 E-Mail: info@amc-systeme.de Web: www.amc-systeme.de

# **Specifications**

#### **Analog Input**

Channels 4, Differential or pseudo-differential (50  $\Omega$  between negative input terminal and chassis ground)

Resolution 24 bits

Input range ±12 V, ±6 V, ±3 V, ±1.5 V, ±0.75 V, ±0.375 V, or ±0.1875 V, software

selectable per channel

 Maximum input voltage • Input common-mode voltage range

Over-voltage protection

Input coupling AC or DC, software configurable

per channel

±12 V

±12 V

±25 V

- Acquisition type Instant or buffered, software

configurable

# Integrated electronic piezoelectric excitation (IEPE) (1)

Accuracy Compliance

- Current

Fault detection threshold Status report

0 mA or 2 mA, software configurable per channel

±5% max. 22 V min.

< 1.5 V (short) and > 22.5 V (open), Software polling

## **Analog low-pass filter**

 Filter type 4-th order, linear phase

- -3 dB bandwidth 1 MHz

 Digital low-pass filter Filter type: Wideband

-3 dB bandwidth: 0.433 x sample

#### Absolute accuracy (2)

auto-calibration temperature ±0.01% of full-scale range max. Over full operating temperature range ±0.05% of full-scale range max.

 Common-mode rejection ratio (CMRR) TBD dB

## DC performance

**ADVANTECH** 

Idle channel noise TBD mVRMS Effective resolution TBD bits

#### **AC** performance

Signal-to-noise ratio (SNR) 110 61 dB Total harmonic distortion (THD) -115.24 dB Total harmonic distortion plus -109.4 dB noise (THD+N)

Spurious-free dynamic range 117.49 dB (SFDR)

Crosstalk -104.06 dB

## **Buffered acquisition**

 Enabled channel combination Each channel can be enabled/disabled

independently by software

(256 / 2n) kHz, where  $n = 0 \sim 8$ , for all Sample rate

channels, simultaneous sampling, software configurable

Internal data buffer (FIFO) size 512 samples

#### **Power Requirement**

**Power Input** 5 V<sub>DC</sub> through iDAQ chassis

Power Consumption

#### Mechanical

- Module dimensions 100 x 80 x 25 mm (3.94 x 3.15 x 0.98 in)

Weight

#### **Environment**

**Operating temperature** -20 °C to 60 °C (-4 °F to 140 °F) -40 °C to 70 °C (-40 °F to 158 °F) Storage temperature **Operating humidity** 10% to 90% RH, non-condensing Storage humidity 5% to 95% RH, non-condensing

Vibration 5Grms Shock 30G

Indoor use only

# Certification

- EMC CE. FCC Safety

# **Ordering Information**

iDAQ-801-AE 4-ch, 24-bit, 256kS/s/ch, DSA iDAQ Module

#### **Optional Accessories**

PCL-1010B-1E BNC Coax Cable, 1m

(1) Input coupling must be AC and input configuration must be pseudo-differential when IFPF is enabled

(2) Operating temperature within ±5°C of last



