PXIe-4143 Specifications

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Integration Partner

SYSTEM INTEGRATOR

Contents

PXIe-4143 Specifications

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These specifications apply to the PXIe-4143.

Definitions

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty.

Warranted specifications describe the performance of a model under stated operating conditions and are covered by the model warranty. Warranted specifications account for measurement uncertainties, temperature drift, and aging. Warranted specifications are ensured by design or verified during production and calibration.

Characteristics describe values that are relevant to the use of the model under stated operating conditions but are not covered by the model warranty.

- **Typical** specifications describe the performance met by a majority of models.
- **Typical-95** specifications describe the performance met by 95% (≈2σ) of models with a 95% confidence.
- **Nominal** specifications describe an attribute that is based on design, conformance testing, or supplemental testing.
- **Measured** specifications describe the measured performance of a representative model.

Specifications are **Warranted** unless otherwise noted.

Conditions

Specifications are valid under the following conditions unless otherwise noted.

- Ambient temperature[1] of 23 °C ± 5 °C
- Calibration interval of 1 year
- 30 minutes warm-up time
- Self-calibration performed within the last 24 hours
- niDCPower Aperture Time property or NIDCPOWER_ATTR_APERTURE_TIME attribute set to 2 power-line cycles (PLC)
- Fans set to the highest setting if the PXI Express chassis has multiple fan speed settings

Device Capabilities

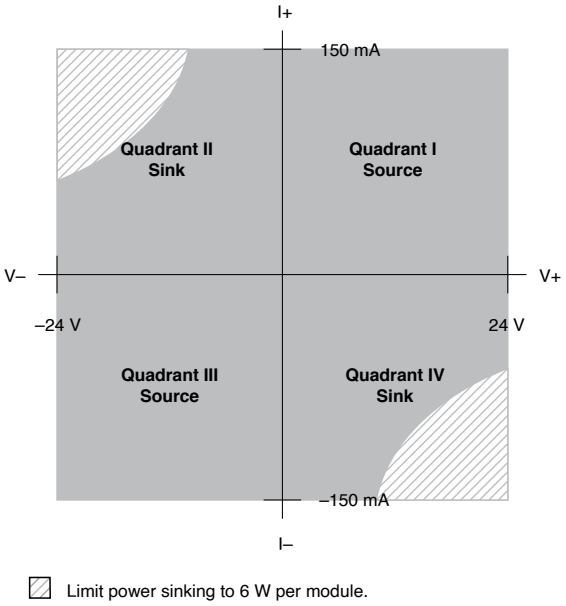
The following table and figure illustrate the voltage and the current source and sink ranges of the PXIe-4143.

Table 1. PXIe-4143 Current Source and Sink Ranges

Channels	DC Voltage Ranges	DC Current Source and Sink Ranges
0 through 3	±24 V	▪ 10 µA ▪ 100 µA ▪ 1 mA ▪ 10 mA ▪ 150 mA

[1] The ambient temperature of a PXI system is defined as the temperature at the chassis fan inlet (air intake).

Figure 1. PXIe-4143 Quadrant Diagram, All Channels



SMU Specifications

Voltage Programming and Measurement Accuracy/Resolution

Table 2. Voltage Programming and Measurement Accuracy/Resolution

Range	Resolution and noise (0.1 Hz to 10 Hz)	Accuracy (23 °C ± 5 °C) ± (% of voltage + offset) [2]		Tempco ± (% of voltage + offset)/°C, 0 °C to 55 °C [3]
		T_{cal} ± 5 °C	T_{cal} ± 1 °C	
24 V	20 µV	0.015% + 1.2 mV	0.013% + 300 µV	0.0005% + 1 µV

Related tasks:

- Calculating SMU Resolution

Related reference:

- Additional Specifications

[2] Accuracy is specified for no load output configurations. Refer to Load Regulation and Remote Sense in the **Additional Specifications** section for additional accuracy derating and conditions.

[3] Temperature Coefficient applies beyond 23 °C ± 5 °C within a given tolerance of Tcal.

Current

Table 3. Current Programming and Measurement Accuracy/Resolution

Range	Resolution and noise (0.1 Hz to 10 Hz)	Accuracy (23 °C ± 5 °C) ± (% of current + offset)		Tempco ± (% of current + offset)/°C, 0 °C to 55 °C [4]
		T_{cal} ± 5 °C	T_{cal} ± 1 °C	
10 µA	10 pA	0.03% + 1.6 nA	0.03% + 400 pA	0.002% + 10 pA
100 µA	100 pA	0.03% + 16 nA	0.03% + 4.0 nA	0.002% + 100 pA
1 mA	1 nA	0.03% + 160 nA	0.03% + 40 nA	0.002% + 1.0 nA
10 mA	10 nA	0.03% + 1.6 µA	0.03% + 400 nA	0.002% + 10 nA
150 mA	150 nA	0.03% + 24 µA	0.03% + 6.0 µA	0.002% + 150 nA

Related tasks:

- Calculating SMU Resolution

Related reference:

- Additional Specifications

Output Resistance Programming Accuracy/Resolution, Typical

Table 4. Output Resistance Programming Accuracy/Resolution, Typical

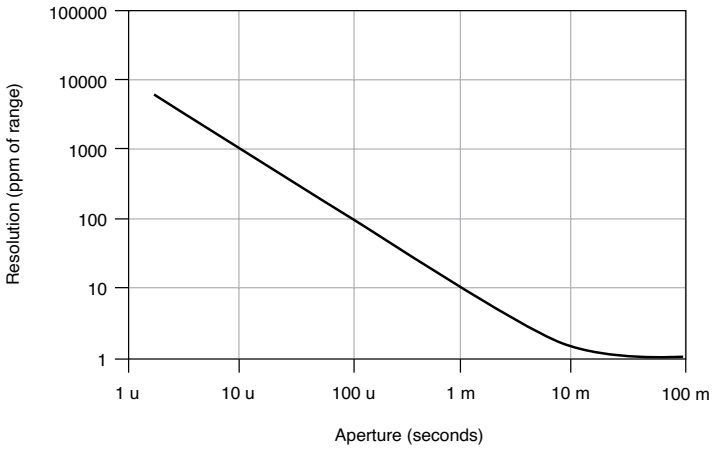
Current limit range	Programmable resistance range	Resolution	Accuracy ± (% of resistance setting), T_{cal} ± 5 °C
10 µA	± 100 kΩ	2 Ω	0.04% + 1.0 Ω
100 µA	± 10 kΩ	200 mΩ	0.04% + 110 mΩ
1 mA	± 1 kΩ	20 mΩ	0.04% + 20 mΩ
10 mA	± 100 Ω	2 mΩ	0.04% + 11 mΩ
150 mA	± 6.66 Ω	120 µΩ	0.04% + 10 mΩ

[4] Temperature Coefficient applies beyond 23 °C ± 5 °C within a given tolerance of Tcal.

Calculating SMU Resolution

Refer to the following figure as you complete the following steps to derive a resolution in absolute units:

Figure 2. Noise and Resolution versus Measurement Aperture, Typical



1. Select a voltage or current range.
2. For a given aperture time, find the corresponding resolution.
3. To convert resolution from ppm of range to absolute units, multiply resolution in ppm of range by the selected range.

Example of Calculating SMU Resolution

The PXIe-4143 has a resolution of 100 ppm when set to a 100 µs aperture time. In the 24 V range, resolution can be calculated by multiplying 24 V by 100 ppm, as shown in the following equation:

$24\ V * 100\ ppm = 24\ V * 100 * 1 \times 10^{-6} = 2.4\ mV$

Likewise, in the 150 mA range, resolution can be calculated by multiplying 150 mA by 100 ppm, as shown in the following equation:

$150\ mA * 100\ ppm = 150\ mA * 100 * 1 \times 10^{-6} = 15\ \mu A$

Additional Specifications

Settling time[5]	<100 µs to settle to 0.1% of voltage step, device configured for fast transient response, typical
Transient response	<100 µs to recover within ±20 mV after a load current change from 10% to 90% of range, device configured for fast transient response, typical
Wideband source noise[6]	2 mV RMS, typical <20 mV_{pk-pk}, typical
Cable guard output impedance	10 kΩ, typical
Remote sense	
Voltage	Add 0.1% of LO lead drop to voltage accuracy specification
Current	Add 0.03% of range per volt of total HI and LO lead drop to current accuracy specification
Maximum lead drop	Up to 1 V drop per lead
Load regulation	
Voltage	10 µV at connector pins per mA of output load when using local sense, typical
Current	20 pA + (10 ppm of range per volt of output change) when using local sense, typical

[5] Current limit set to ≥1 mA and ≥10% of the selected current limit range.
[6] 20 Hz to 20 MHz bandwidth. PXIe-4143 configured for normal transient response.

Isolation voltage, channel-to-earth ground	60 VDC, CAT I, verified by dielectric withstand test, 5 s, continuous, characteristic
Absolute maximum voltage between any terminal and LO	30 VDC, continuous

The following figures illustrate the effect of the transient response setting on the step response of the PXIe-4143 for different loads.

Figure 3. 1 mA Range No Load Step Response, Typical

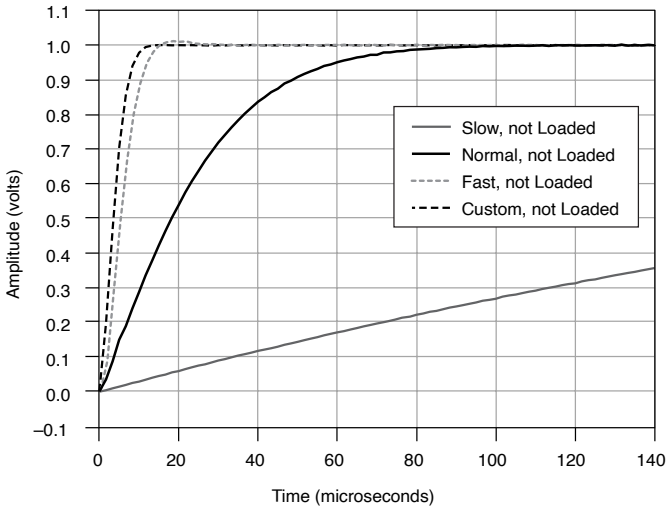
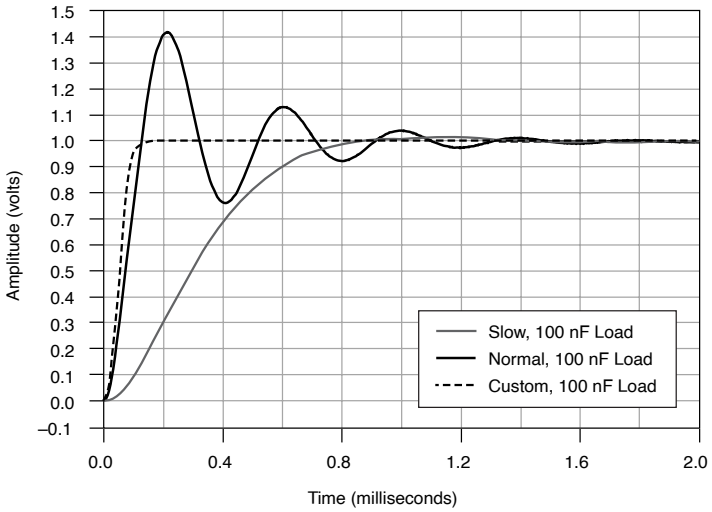


Figure 4. 1 mA Range, 100 nF Load Step Response, Typical



Related reference:

- Voltage Programming and Measurement Accuracy/Resolution
- Current

Supplemental Specifications

Measurement and Update Timing

Available sample rates[7]	(600 kS/s)/N

where

- N = 6, 7, 8, … 2^{20}
- S is samples

Sample rate accuracy	±50 ppm
Maximum measure rate to host[8]	600,000 S/s per channel, continuous
Maximum source update rate[9]	
Sequence length <300 steps per iteration	100,000 updates/s per channel
Sequence length ≥300 steps per iteration	100,000 updates/s per board
Input trigger to	
Source event delay	5 µs
Source event jitter	1.7 µs

[7] When source-measuring, both the NI-DCPower Source Delay and Aperture Time properties affect the sampling rate. When taking a measure record, only the Aperture Time property affects the sampling rate.

[8] Load dependent settling time is not included. Normal DC noise rejection is used.

[9] As the source delay is adjusted or if advanced sequencing is used, maximum source update rates may vary.

Measure event jitter	1.7 µs

Triggers

Input triggers	
Types	Start Source Sequence Advance Measure
Sources (PXI trigger lines 0 to 7)[10][10]	
Polarity	Active high (not configurable)
Minimum pulse width	100 ns
Destinations[11] **(PXI trigger lines 0 to 7)[10]**	
Polarity	Active high (not configurable)
Minimum pulse width	200 ns
Output triggers (events)	
Types	Source Complete Sequence Iteration Complete Sequence Engine Done

[10] Pulse widths and logic levels are compliant with **PXI Express Hardware Specification Revision 1.0 ECN 1**.

[11] Input triggers can come from any source (PXI trigger or software trigger) and be exported to any PXI trigger line. This allows for easier multi-board synchronization regardless of the trigger source.

	Measure Complete
Destinations (PXI trigger lines 0 to 7)[10]	
Polarity	Active high (not configurable)
Pulse width	230 ns

Calibration Interval

Recommended calibration interval	1 year

Physical

Dimensions	3U, one-slot, PXI Express/CompactPCI Express module 2.0 cm × 13.0 cm × 21.6 cm (0.8 in. × 5.1 in. × 8.5 in.)
Weight	
20 W	412 g (14.53 oz)
40 W	428 g (15.1 oz)
Front panel connectors	25-position D-SUB, male

Power Requirement

PXIe-4143 (40W)	3.0 A from the 3.3 V rail and 6.0 A from the 12 V rail

PXIe-4143 (20W)	2.5 A from the 3.3 V rail and 2.7 A from the 12 V rail

Environmental Characteristics

Temperature	
Operating	0 °C to 55 °C
Storage	-40 °C to 70 °C
Humidity	
Operating	10% to 70%, noncondensing. Derate 1.3% per °C above 40 °C.
Storage	5% to 95%, noncondensing.
Pollution Degree	2
Maximum altitude	2,000 m (800 mbar) (at 25 °C ambient temperature)
Shock and Vibration	
Operating vibration	5 Hz to 500 Hz, 0.3 g RMS
Non-operating vibration	5 Hz to 500 Hz, 2.4 g RMS
Operating shock	30 g, half-sine, 11 ms pulse

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